

JON E. WRIGHT
DIRECTOR
202.772.8651
JWRIGHT@SKGF.COM



October 1, 2010

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Confirmation No. 2014
Art Unit 2812
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Re: U.S. Patent No. 7,163,856; Issued: January 16, 2007
(U.S. Patent Application No. 10/714,271; Filing Date: November 13, 2003)
For: **Method of Fabricating a Lateral Double-Diffused MOSFET
(LDMOS) Transistor and a Conventional CMOS Transistor**
Inventors: ZUNIGA *et al.*
Our Ref: 2912.011REX0

Sir:

Transmitted herewith for appropriate action is a Change of Correspondence Address - *Patent*. This document is filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

A handwritten signature in black ink, appearing to read "Jon E. Wright".

Jon E. Wright
Attorney for Patentees
Registration No. 50,720

JEW/CAC/vb
Enclosure

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